

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Attorney Docket Number	3382-64473-01
Application Number	10/620,744
Filing Date	July 15, 2003
First Named Inventor	Srinivasan
Art Unit	2621
Examiner Name	Anand Shashikant Rao

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date and submit a copy.

Examiner's Initials*	Cite No. (optional)	Number	Date	Name of Applicant or Patentee
		5,822,000	10-1998	Yoon
		5,933,541	08-1999	Kutka et al.
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Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee
		Europe	EP0467054B1	01-1992	NEC Corporation

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Examiner's Initials*	Cite No. (optional)	OTHER DOCUMENTS
		ADAMS, "Generalized Reversible Integer-to-Integer Transform Framework," 2003 IEEE Pacific Rim Conference on Communications, Computers, and Signal Processing, Vol. 2, August 2003, pages 1000-1003.
		HAO et al., "Matrix Factorizations for Reversible Integer Mapping," IEEE Transactions on Signal Processing, Vol. 49, Issue 10, October 2001, pages 2314-2324.
		KLAUSUTIS et al., "Variable Block Size Adaptive Lapped Transform-based Image Coding," 1997 International Conference on Image Processing (ICIP '97), Vol. 3, pp. 686-689 (1997).

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